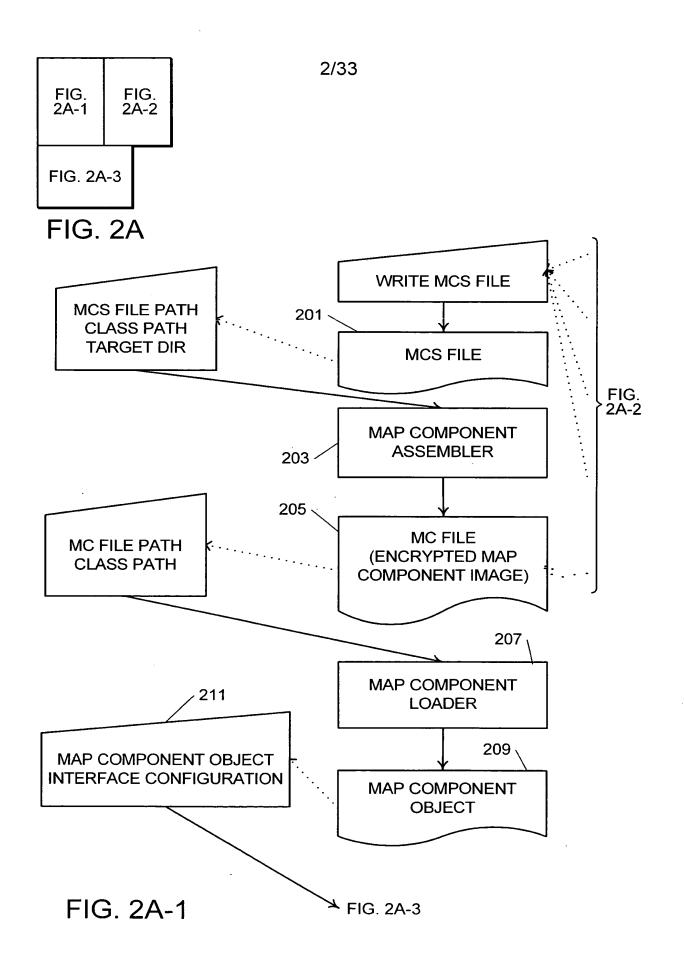


FIG. 1



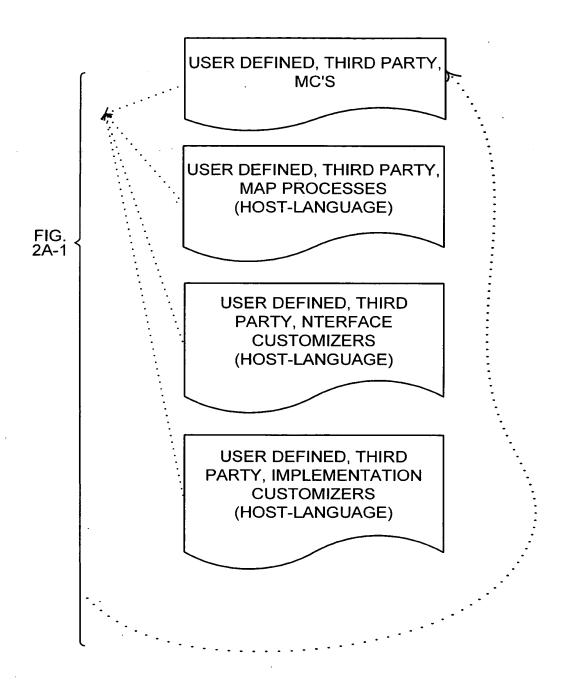
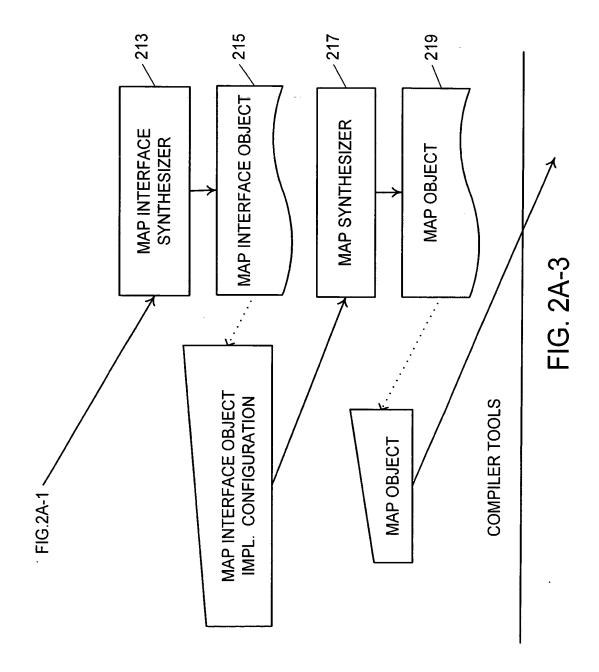
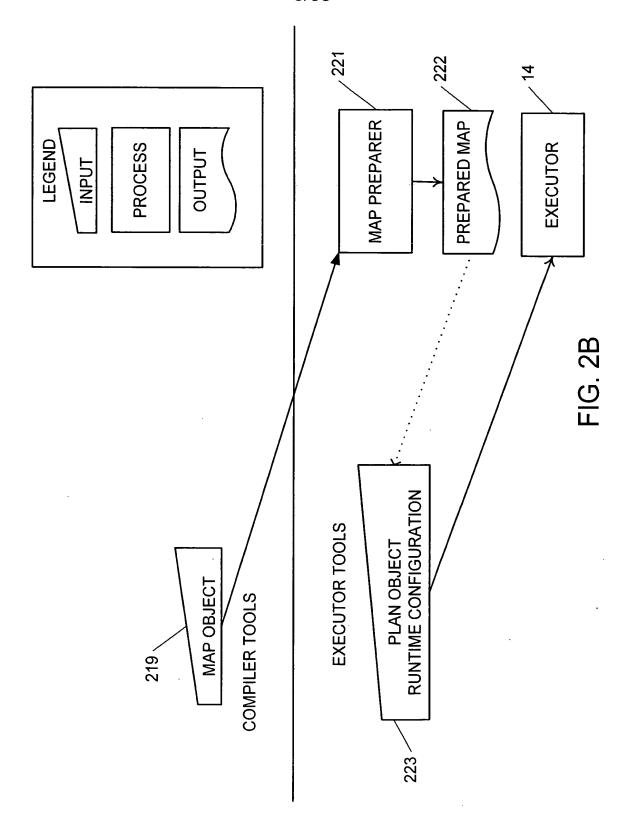


FIG. 2A-2





MAP COMPONENT SOURCE CREATION

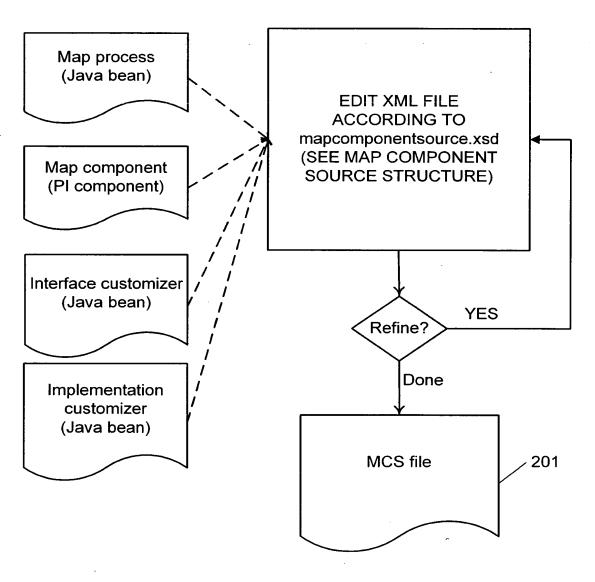


FIG. 3

INTERFACE

DOCUMENTATION

INTERFACE PROPERTY LIST
IMPLEMENTATION PROPERTY LIST
RUNTIME PROPERTY LIST

PORT DECLARATION LIST ...

CUSTOMIZER DECLARATION AND CONFIGURATION

IMPLEMENTATION

INTERNAL MAP COMPONENT INSTANCE DECLARATION LIST... (AND CONFIGURATIONS)

INTERNAL MAP PROCESS INSTANCE DECLARATION LIST... (AND CONFIGURATIONS)

PORT TO PORT LINK LIST...

CUSTOMIZER DECLARATION AND CONFIGURATION

FIG. 4

MAP COMPONENT ASSEMBLY PROCESS

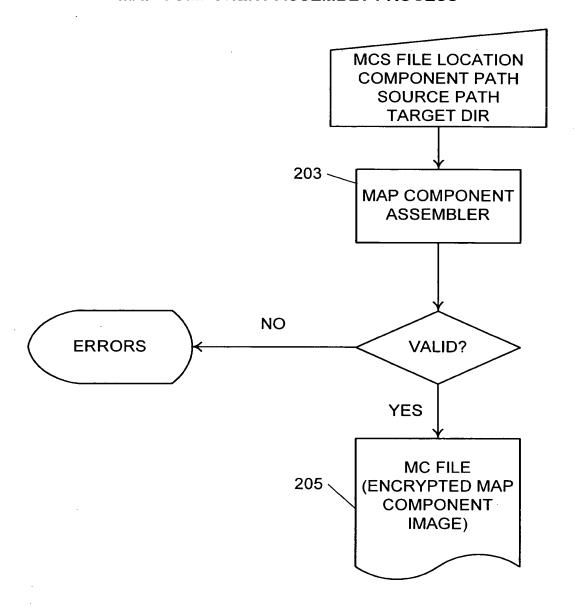


FIG. 5

MAP COMPONENT

DOCUMENTATION

INTERFACE PROPERTY LIST IMPLEMENTATION PROPERTY LIST RUNTIME PROPERTY LIST

ENCRYPTED CONFIGURABLE SPECIFICATION

FIG. 6

MAP COMPONENT LOADING

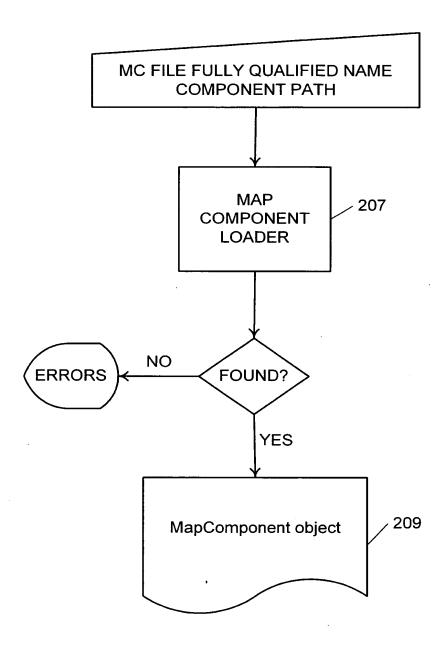


FIG. 7

MAP INTERFACE SYNTHESIS

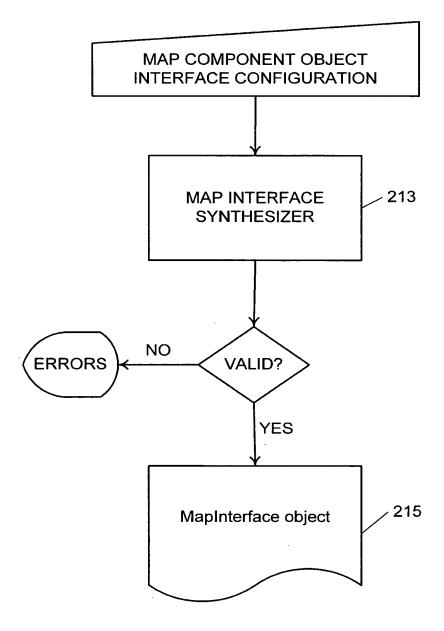


FIG. 8

MAP INTERFACE

DOCUMENTATION

INTERFACE CONFIGURATION IMPLEMENTATION PROPERTY LIST RUNTIME PROPERTY LIST

PORT LIST...

FIG. 9

MAP SYNTHESIS

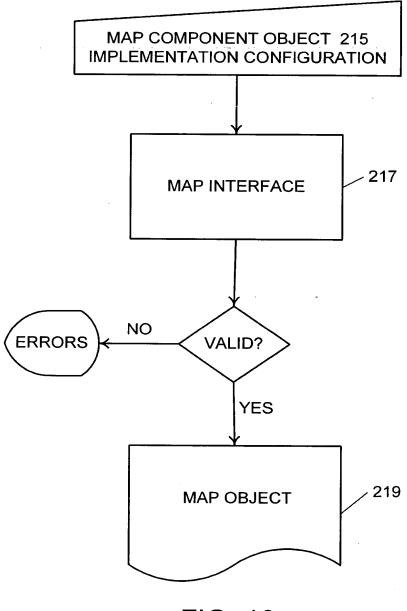


FIG. 10

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MAP

DOCUMENTATION

INTERFACE CONFIGURATION IMPLEMENTATION CONFIGURATION RUNTIME PROPERTY LIST

INTERFACE

PORT DECLARATION LIST ...

IMPLEMENTATION (if open)

INTERNAL MAP COMPONENT INSTANCE DECLARATION LIST... (AND CONFIGURATIONS)

INTERNAL MAP PROCESS INSTANCE DECLARATION LIST... (AND CONFIGURATIONS)

PORT TO PORT LINK LIST...

INTERNAL MAP LIST ...

FIG. 11

	TYPE, ID	· .
	,	
	INPUT PORT LIST	
0	UTPUT PORTS	
	OUTPUT PORT	٦
	OUTPUT PORT TYPE	
	INPUT PORT READER LIST	
0	UTPUT PORT LIST	
	MAP PROCESS LIST	

FIG. 12

MAP PREPARATION

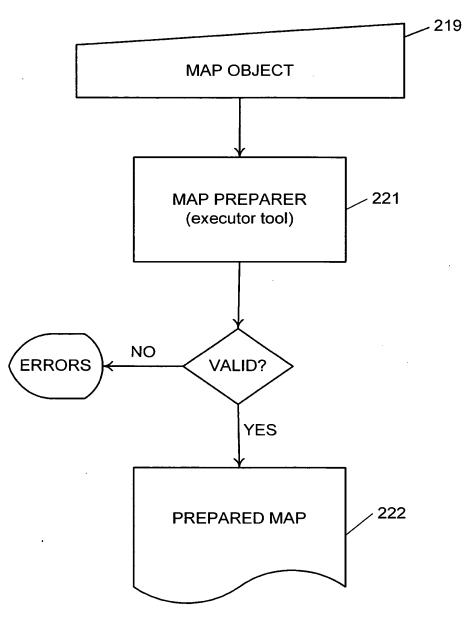


FIG. 13

PREPARED MAP EXECUTION

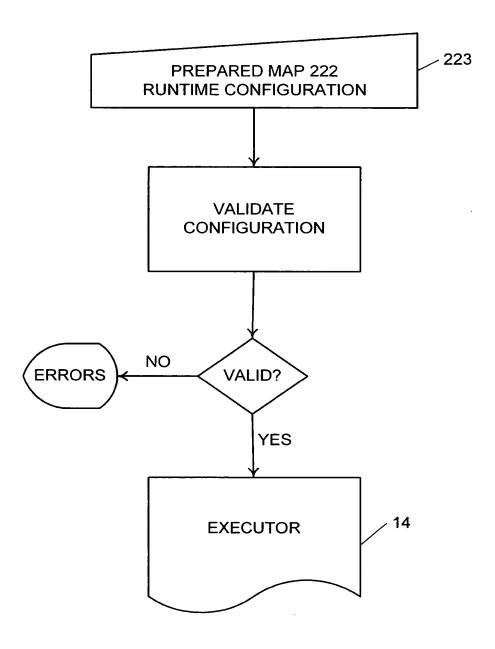


FIG. 14

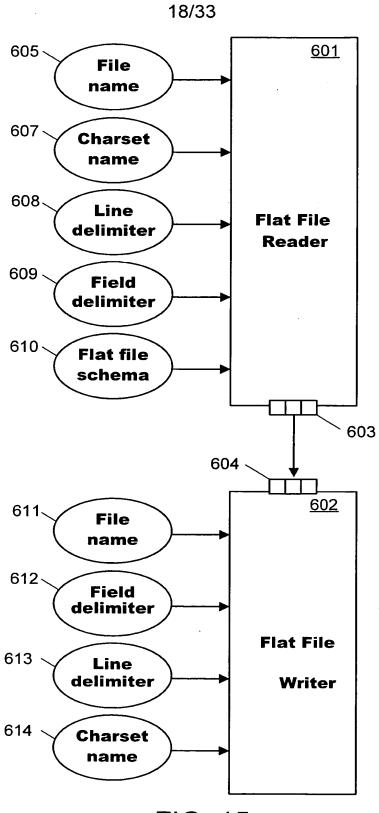
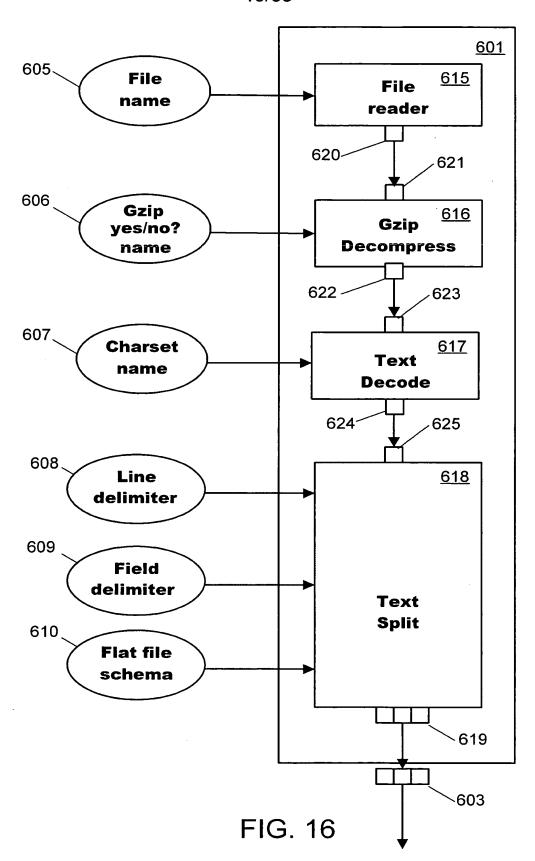
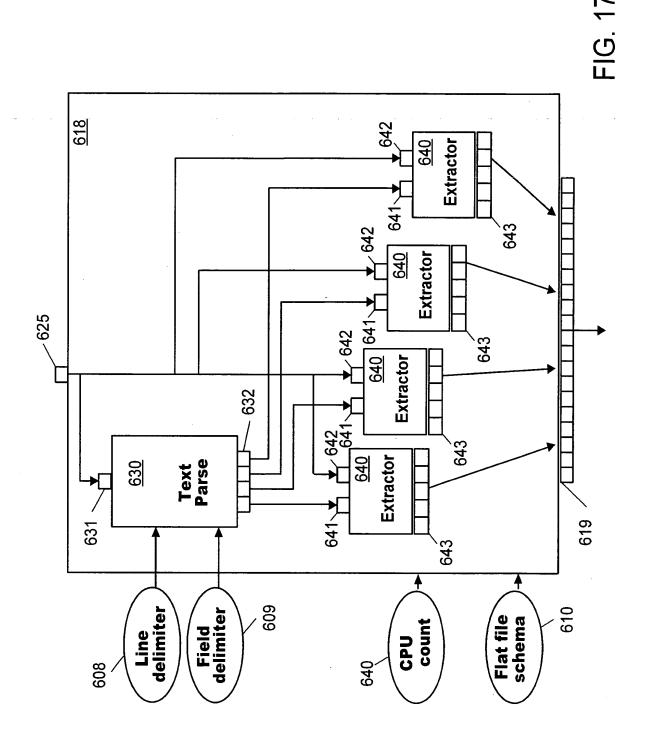


FIG. 15





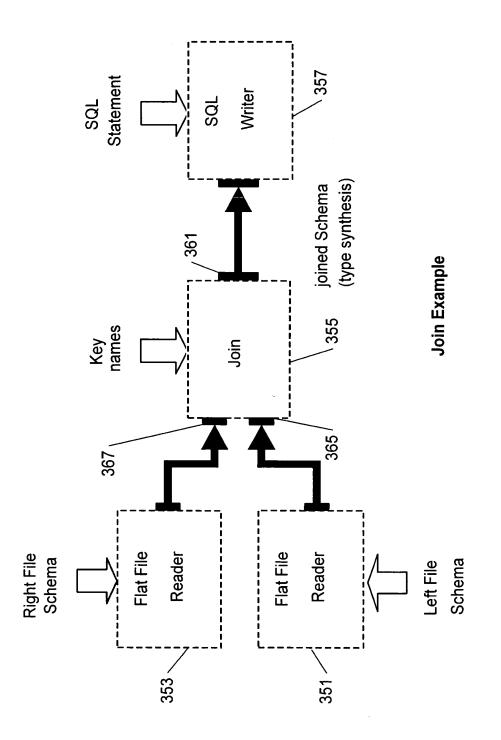
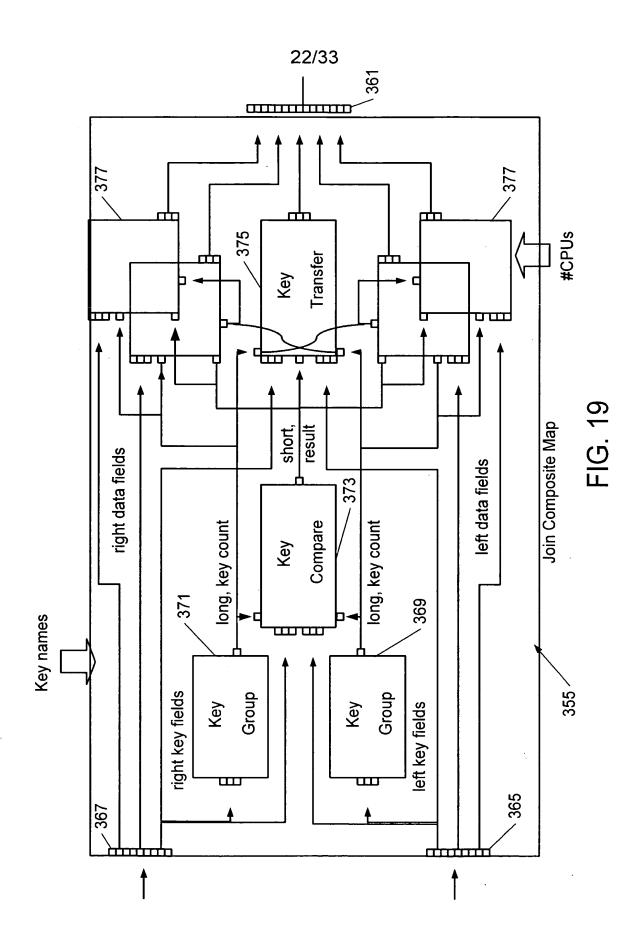
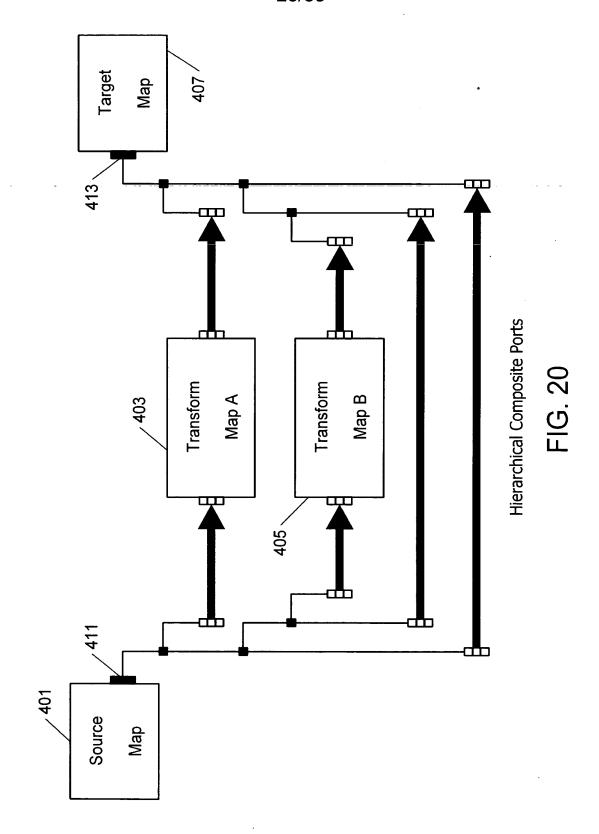


FIG. 18





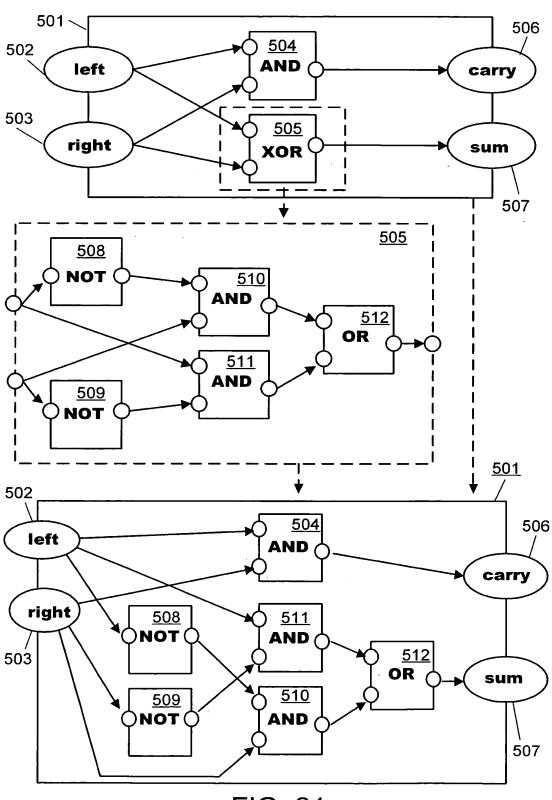
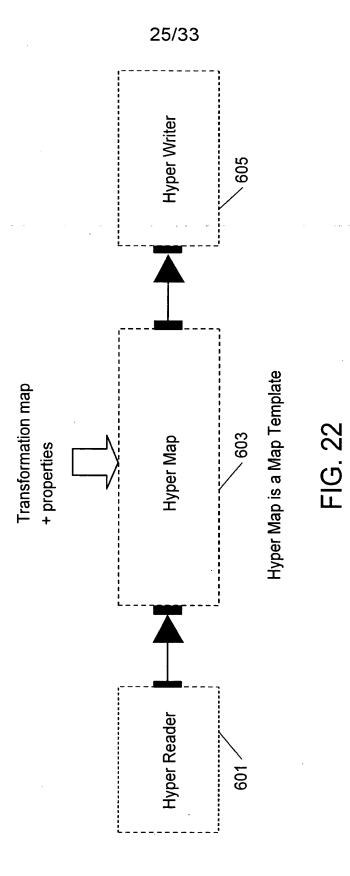
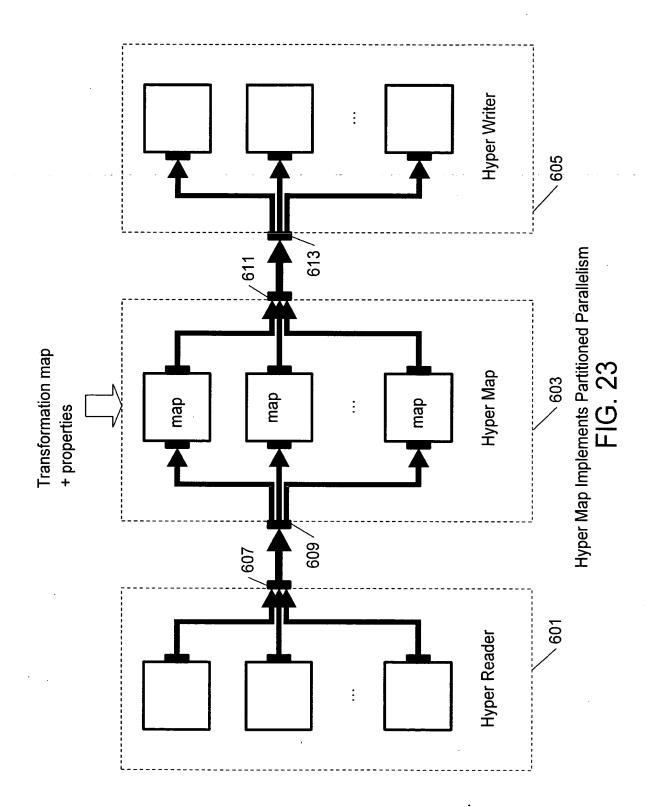
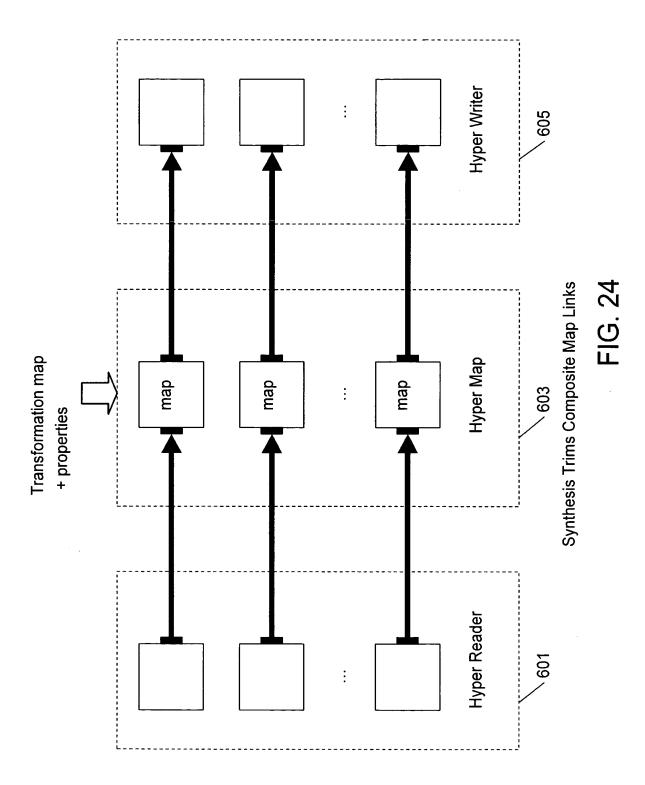
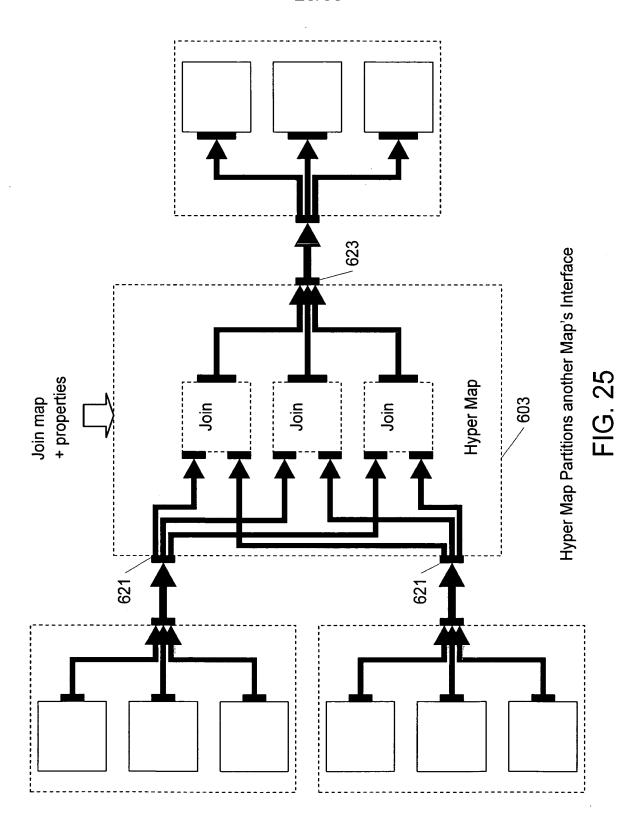


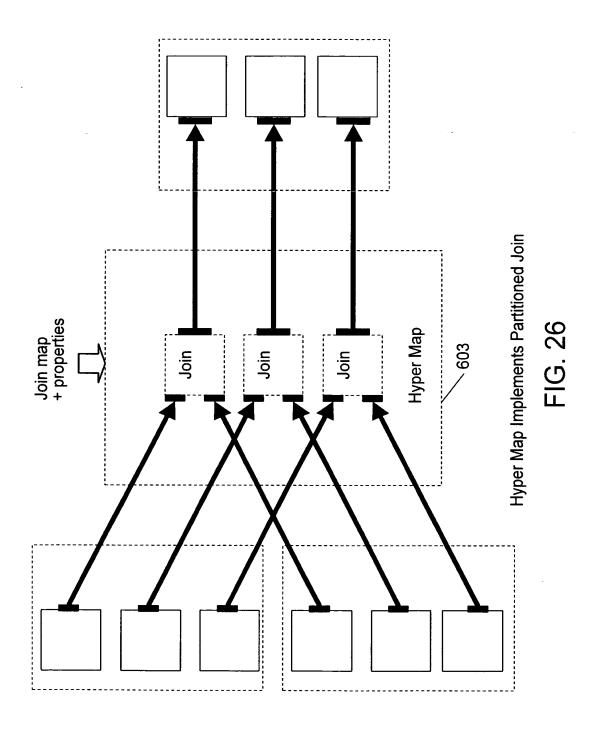
FIG. 21











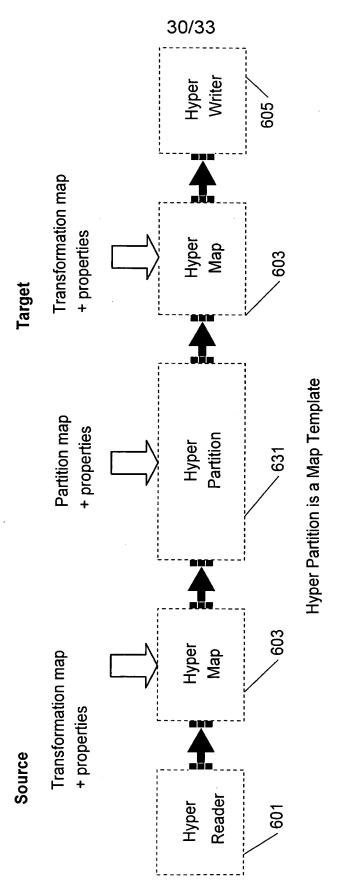
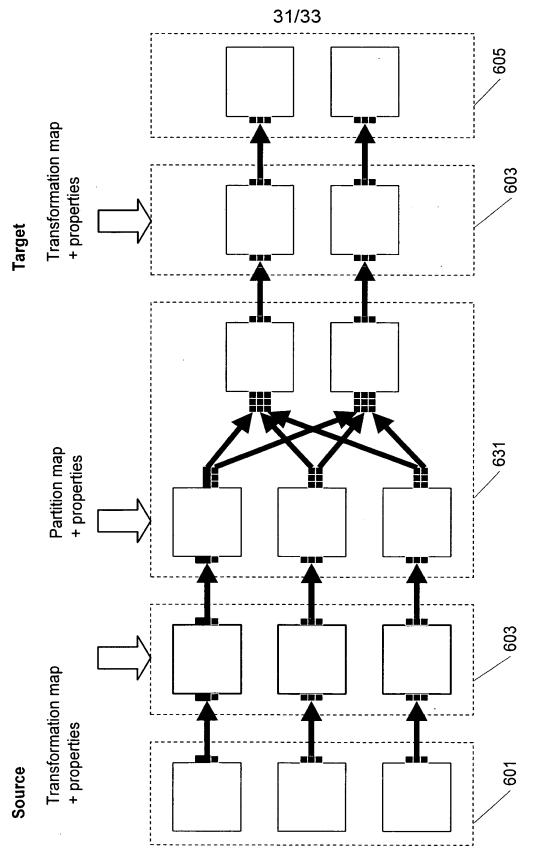


FIG. 27



Hyper Partition corrects Partition Schema Mismatch FIG. 28

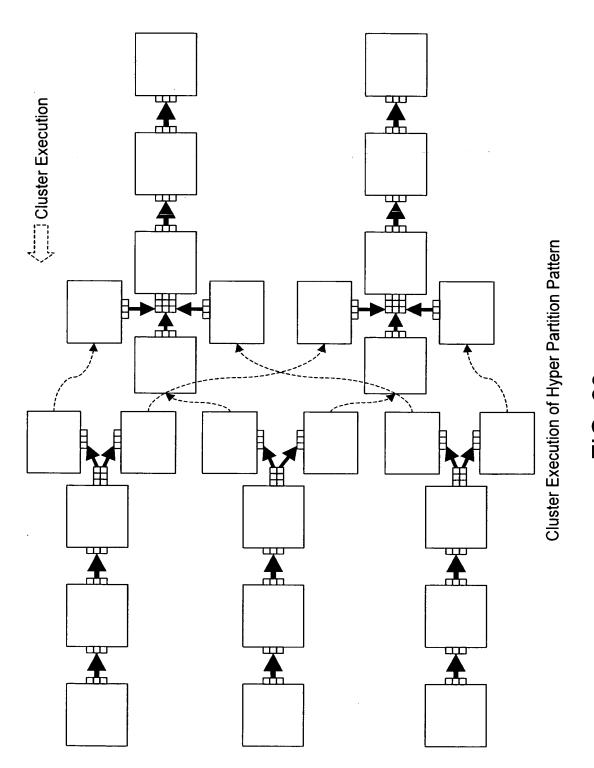


FIG. 29

